

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Substitute for form 1449/PTO)			ATTY. DOCKET NO. <b>043890-0745</b>		SERIAL NO. <b>10/541,320</b>	
			APPLICANT <b>Keisuke KURODA, et al.</b>			
			FILING DATE <b>July 11, 2006</b>		GROUP <b>2856</b>	
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sub>2</sub> ( <i>if known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	US					
	US					
<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code <sub>3</sub> -Number <sub>4</sub> -Kind Code <sub>5</sub> ( <i>if known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes      No
		JP 9-281138	10-31-1997	MATSUSHITA ELECTRIC IND CO LTD		JAPAN (w/English Abstract)
		EP 0 947 803 A2	10-06-1999	MATSUSHITA ELECTRIC IND CO LTD		X
		WO 02/29895 A1	04-11-2002	ROCKWELL SCIENTIFIC COMPANY		X
<b>OTHER ART</b> (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
		Supplementary European Search Report issued in European Patent Application No. EP 05 70 9669, dated October 28, 2010.				
		ROUBIK GREGORIAN, et al., "Switched-Capacitor Circuit Design", Proceeding of the IEEE Vol. 71, No. 8, August 1983, IEEE.				
		DAVID L. FRIED, "Analog Sample-Data Filters", IEEE Journal of Solid-State Circuits, August 1972.				
EXAMINER			DATE CONSIDERED			

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